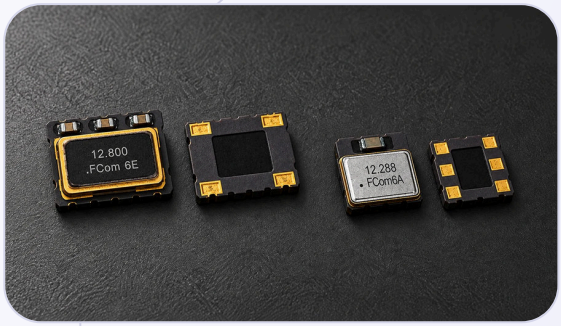
TEMPERATURE COMPENSATED CRYSTAL OSCILLATORS

TCXO

Product Selection Guide

Compact SMD & High Precision

Stratum 3 & Low Phase Noise



Standard SMD | High Precision | Low Jitter / Low Noise | High Temperature

One function. A choice of stability and output.

Compare the full TCXO range in a consistent, series-by-series list. Review package, frequency range, supply voltage, stability, temperature and phase noise before selecting a configuration.

10	04	2016	+105 °C
Individual series in one guide	Product groups for comparison	Smallest SMD package option	Maximum operating temperature

5 SERIES

Standard SMD TCXO

FVT-6S, FVT-2S, FVT-3S, FVT-5S and FVT-7S in 2016 to 7050 packages.

Full selection list · Page 02

3 SERIES

High Precision TCXO

FVT-5S-HP, FVT-7S-HP and FVT-7S-WT with tight stability and extended temperature.

High precision selection · Page 03

2 SERIES

Low Jitter / Low Noise

FVT-3S-LJ and FVT-9S-LN for phase-noise-sensitive applications.

Low noise selection · Page 03

Compare the series. Confirm the configuration.

Click a series name or View PDF to open its datasheet. Confirm the latest controlled specifications before placing an order.

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01 Standard SMD TCXO

Temperature compensated crystal oscillators in 2016, 2520, 3225, 5032 and 7050 SMD packages.
Each series is listed separately for direct comparison.

Output: CMOS · Supply: 1.8 V / 2.5 V / 3.3 V — **5 SERIES · SURFACE MOUNT**

Photo	Series	Package	Frequency range MHz	Supply voltage V	Stability ±ppm	Widest temp. °C	Output	Phase noise @1 kHz dBc/Hz	Datasheet PDF
	FVT-6S	**2016** 2-SMD	10 - 52	1.8 / 2.5 / 3.3	±0.5 to ±2.5	-40 to +85	CMOS	-145	View PDF ↗
	FVT-2S	**2520** 4-SMD	10 - 52	1.8 / 2.5 / 3.3	±0.5 to ±2.5	-40 to +85	CMOS	-145	View PDF ↗
	FVT-3S	**3225** 4-SMD	10 - 52	1.8 / 2.5 / 3.3	±0.5 to ±2.5	-40 to +85	CMOS	-145	View PDF ↗
	FVT-5S	**5032** 4-SMD	10 - 52	1.8 / 2.5 / 3.3	±0.5 to ±2.5	-40 to +85	CMOS	-145	View PDF ↗
	FVT-7S	**7050** 4-SMD	10 - 52	1.8 / 2.5 / 3.3	±0.5 to ±2.5	-40 to +85	CMOS	-145	View PDF ↗

Product images are for identification. Refer to the series drawing for exact dimensions, pad assignments and test conditions.

Read the parameters together

1. Stability figures describe the frequency tolerance against a stated reference condition. They are not necessarily available in every combination with supply voltage, temperature range and output load.

2. Phase noise figures are typical values at the stated offset. Confirm the measurement bandwidth reference, output load and supply voltage before comparing series.

3. The 2016 and 2520 packages use different pad counts from the 3225 and larger packages. Confirm the footprint and pad assignment in the series datasheet before layout.

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02

High Precision, Low Jitter & Low Phase Noise TCXO

Extended temperature, tight stability and phase-noise-optimized series in 3225, 5032, 7050 and 1409 packages.

Output: CMOS · Supply: 2.5 V / 3.3 V — **5 SERIES · SURFACE MOUNT**

Photo	Series	Package	Frequency range MHz	Supply voltage V	Stability ±ppm	Widest temp. °C	Output	Phase noise @1 kHz dBc/Hz	Datasheet PDF
High precision & high temperature									3 SERIES
	FVT-7S-WT	**7050** 4-SMD	10 - 52	2.5 / 3.3	±0.5 to ±2.5	-40 to +105	CMOS	-145	View PDF ↗
	FVT-5S-HP	**5032** 4-SMD	10 - 52	2.5 / 3.3	±0.28 (Stratum 3)	-40 to +85	CMOS	-150	View PDF ↗
	FVT-7S-HP	**7050** 4-SMD	10 - 52	2.5 / 3.3	±0.28	-40 to +85	CMOS	-150	View PDF ↗
Low jitter & low phase noise									2 SERIES
	FVT-3S-LJ	**3225** 4-SMD	10 - 52	2.5 / 3.3	±0.5 to ±2.5	-40 to +85	CMOS	-155	View PDF ↗
	FVT-9S-LN	**1409** 4-SMD	10 - 52	3.3	±0.28	-40 to +85	CMOS	-160	View PDF ↗

The 1409 package uses a larger footprint. Confirm the layout and thermal considerations for high-precision and low phase noise series. Images are for identification and are not to scale.

Compact designs

FVT-6S · FVT-2S · FVT-3S

2016 to 3225 packages with ±0.5 ppm stability options for space-constrained boards.

Timing & sync

FVT-5S-HP · FVT-7S-HP

Stratum 3 and high precision TCXO for network synchronization and base stations.

Low noise

FVT-3S-LJ · FVT-9S-LN

Phase-noise optimized series for RF, instrumentation and microwave reference designs.

Before selecting an ordering code

1. Confirm the stability reference temperature and compatibility of stability, supply voltage and operating temperature options. Tight stability is not available across every combination.

2. Confirm phase noise at the required offset and measurement bandwidth. Values quoted at 1 kHz do not describe performance at 100 Hz or 100 kHz.

3. High precision and low phase noise series may require additional supply filtering and careful PCB layout. Confirm the recommended decoupling and layout guidelines in the series datasheet.

Need help matching a TCXO?

Share your preferred series, stability, temperature range, output load and oscillator IC with FCom for specification review.

Technical support ↗

Visit FCom ↗

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