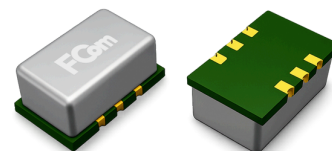


FCom TCXO

Series FVT-9S-LN

Low Noise TCXO 14×9 mm



Feature

- Low Phase Noise
- High Stability for Stratum 3
- Small SMD Package

Application

- Time Synchronization
- Microwave Communication
- Test & Measurement
- Telecom Systems
- Satellite Communication

ELECTRICAL SPECIFICATIONS

Parameters		Symbol	Min.	Typ.	Max.	Units	Notes
Frequency (Fo)		F	40	-	150	MHz	
Frequency Stability (Overall)		Fs	±4.6			ppm	
Holdover		-	-0.37	-	+0.37	ppm	Over 24 Hours
Initial Tolerance		-	-1.0	-	+1.0	ppm	at 25°C
Operating Temperature Range		Topr	-40°C~ +85°C			°C	
Storage Temperature Range		Tstg	-40°C~ +105°C			°C	
Waveform		-	Sine Wave			-	
Level		-		+8		dBm	
Load		-		50		Ω	
Harmonics			-	-	-30	dBc	
Waveform		-	CMOS			-	
Output Level	Output High	VH	2.97	-	-	V	
	Output Low	VL	-	-	0.33	V	
	Duty Cycle	TH/T	45		55	%	
Rise/Fall Time		Tr/Tf	-	-	3	nSec	
Load		CL	-	15	-	pF	
Control Voltage Range (VCTCXO)		-	0.5	-	2.5	V	
Pulling Range (VCTCXO)		-	±5	-	-	ppm	
Vc Input Impedance (VCTCXO)		-	100	-	-	kΩ	
Phase Noise (Max.)	10Hz	PN			-87	dBc/Hz	
	100Hz				-117		
	1KHz				-141		
	10KHz				-148		
	100KHz				-155		
	1MKHz				-160		

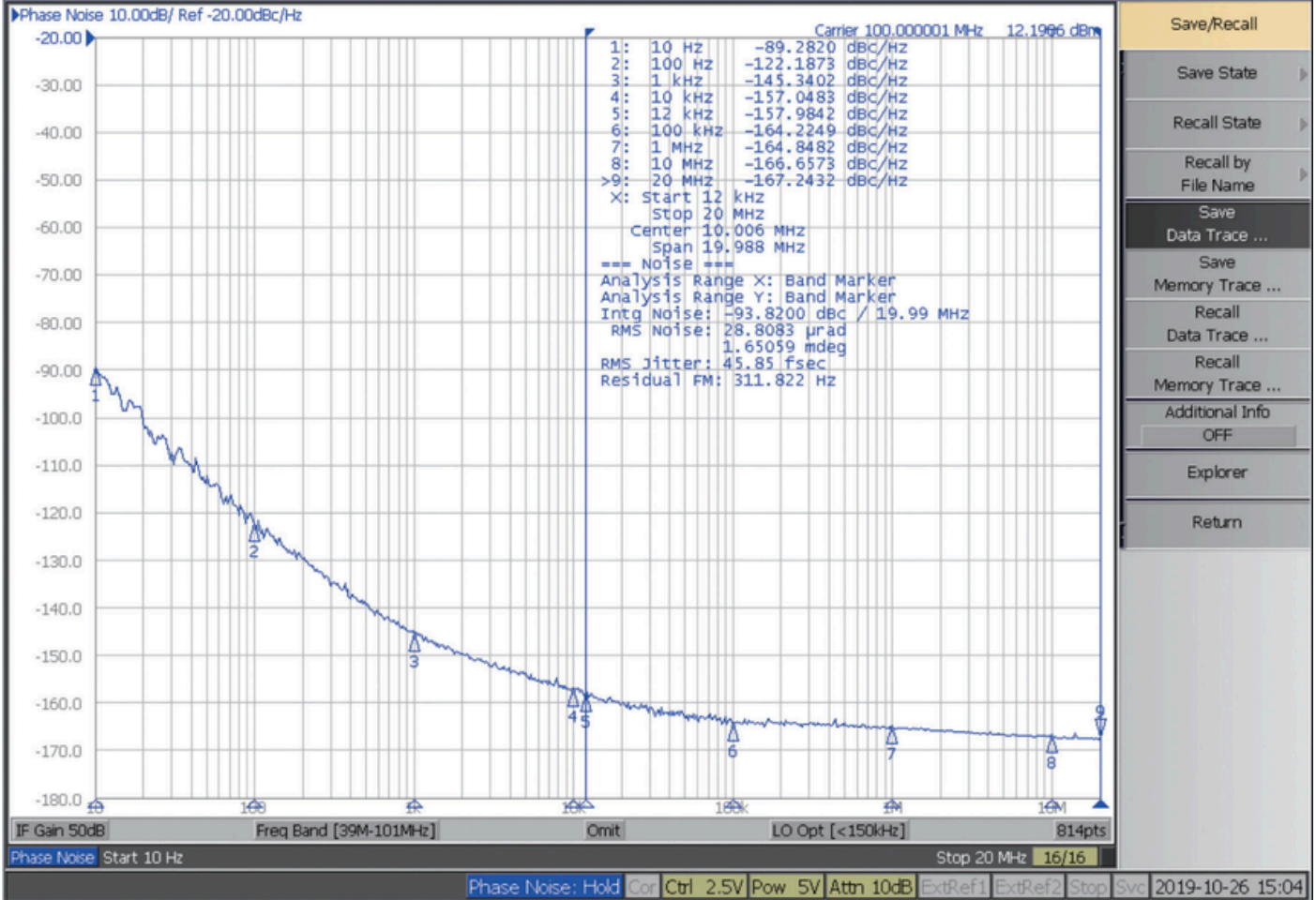
Note : The table shows common spec. if you have special spec. requirement , please feel free to contact our salesperson.

INPUT POWER (PIN = VDD)

Parameter	Min.	Typ.	Max.	Units	Test Condition
Voltage		+3.3		V	
Current			35	mA	At maximum supply voltage

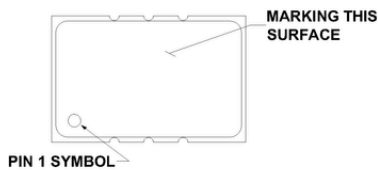
PHASE NOISE TEST DATA

Agilent E5052B Signal Source Analyzer

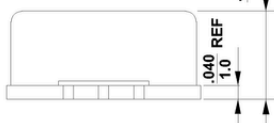


DIMENSION(mm)

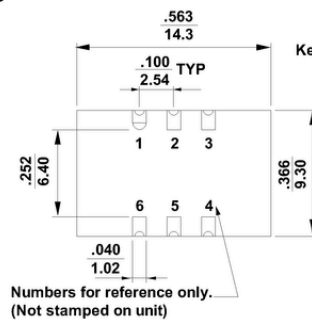
TOP VIEW



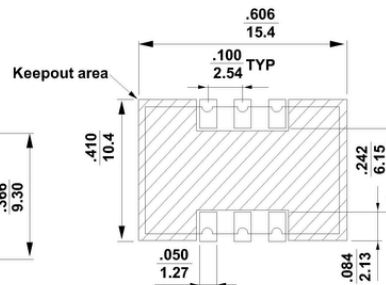
SIDE VIEW



BOTTOM VIEW



Solder Pad Layout(mm)



Note:
1.If the specification does not specify parameters for either PIN 1,PIN 2 then that respective PIN is NOT internally CONNECTED.

INCH
mm (REFERENCE ONLY)

PIN CONNECTIONS	
PIN	FUNCTION
1 (See Note 1)	VCO INPUT or NOT CONNECTED
2 (See Note 1)	REFERENCE VOLTAGE or NOT CONNECTED
3	0 VOLTS AND CASE
4	R.F.OUTPUT
5	NOT CONNECTION
6	+VDC