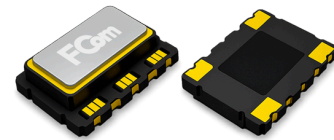


FCom TCXO

Series FVT-7S

Standard TCXO 7.0×5.0 mm



Feature

- Typical 7.0 x 5.0 x 1.9 mm ceramic SMD package
- For automatic assembly
- Compactness and lightweight
- Low power consumption

Application

- Femtocell , Base Stations
- WiMAX, WLAN, WIFI
- Wireless Communications
- Mobile Phone

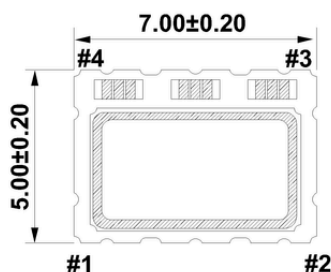
ELECTRICAL SPECIFICATIONS

Parameters		Symbol	3.3 /3.0		2.5V		Units	Notes
			Min.	Max.	Min.	Max.		
Frequency Range		F	5~52				MHz	
Standard Frequency	CMOS	F	5, 6.4, 8, 8.192, 10, 12.5, 12.8, 16, 16.384, 19.44, 25, 26				MHz	
	Clipped Sine Wave		8, 8.192, 10, 12.5, 12.8, 16, 16.384, 19.44, 25, 26					
Supply Current	CMOS	Icc	-	6.0	-	6.0	mA	
	Clipped Sine Wave		-	3.5	-	3.5		
Frequency Tolerance		Ft	-	±2.0	-	±2.0	ppm	
Frequency stability	vs. Vdd	Fs	-	±0.1	-	±0.1	ppm	±5%
	vs. Load		-	±0.2	-	±0.2		±10%
	vs. Aging		-	±1.0	-	±1.0		@1st year
Output Level		Vp-p	0.8	-	0.8	-	V	Clipped Sine Wave
Control Voltage Range			0.5	2.5	0.4	2.4	V	VCTCXO
Pulling Range			±5.0	±12.0	±5.0	±12.0	ppm	VCTCXO
Vc Input Impedance			100	-	100	-	kΩ	VCTCXO
Phase Noise @ 19.2 MHz	100 Hz	PN	-115		-115		dBc/Hz	
	1 kHz		-135		-135			
	10 kHz		-148		-148			
Output Loading	CMOS	RL // CL	15pF				pF	
	Clipped Sine Wave		10 KΩ // 10pF					
Start-up Time		Tosc	2				mSec	Max

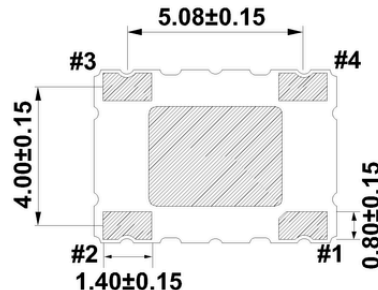
! Note : The table shows common spec. if you have special spec. requirement , please feel free to contact our salesperson.

DIMENSION(mm)

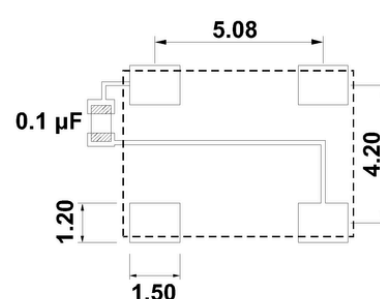
TOP VIEW



BOTTOM VIEW

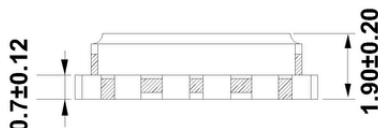


Solder Pad Layout(mm)



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1μF as close to the part as possible between Vdd and GND pads.

SIDE VIEW



PIN#	Function
1	VCON: VC-TCXO GND/NC: TCXO
2	GND
3	Output
4	VDD

○: Available △: Conditional ×: Not available
Inclusive of calibration @25°C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration.

FREQ. STABILITY vs. TEMP. RANGE

Temp.(°C)	ppm	± 0.5	± 1.0
@-20~70°C	○	○	
@-30~85°C	○	○	
@-40~85°C	△	○	