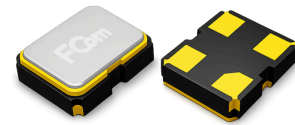


FCom Crystal Oscillator

Series FCO-6C

Standard Clock Oscillator 2.0×1.6 mm



Feature

- Typical 2.05 x 1.65 x 0.75 mm package
- Tight symmetry (45 to 55%) available
- Operation voltage: 1.8V, 2.5V, 3.3V
- Tri-state enable/disable

Application

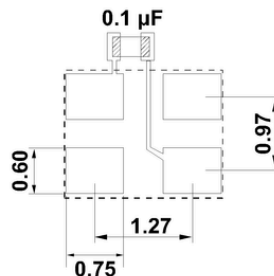
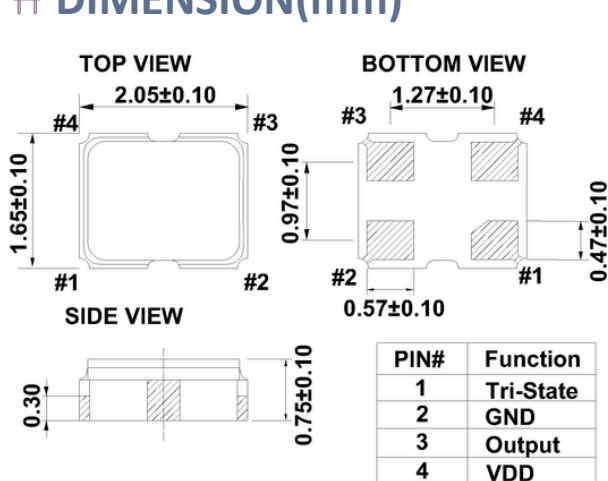
- WLAN/WiMax
- Mobile Phone
- DSC, Set-top Box, HDTV

ELECTRICAL SPECIFICATIONS

Parameters		Symbol	Min.	Typ.	Max.	Units	Notes	
Frequency Range		F	1.5~50			MHz		Test condition Ambient temperature: 25 ± 5°C Relative humidity: 40% ~ 70%
Standard Frequency		F(ST)	24, 26			MHz		
Supply Voltage		Vdd	1.8V	2.5V	3.3V	V	Typ.	
Current Consumption @15pF Load		Icc	7	10	15	mA	Max.	
Rise Time /Fall Time	1.5 MHz ≤ Fo < 20 MHz	Tr/Tf	5	4	4	nSec	Max. @10% ~ 90% @ load 15pF	
	20 MHz ≤ Fo < 50 MHz		4	3	3			
Stand by Current		Icc(ST)	-	-	10	uA		
Output Load (CMOS)		CL	15			pF		
Output Level		VoH	90% Vdd	-	-	V	Output High	
		VoL	-	-	10% Vdd	V	Output Low	
Tri-State Mode (Input to Pin 1)		VIH	70% Vdd	-	-	V	Enable	
		VIL	-	-	30% Vdd	V	Disable	
Period Jitter		Pk	-	-	40	pSec		
RMS Phase Jitter		PJ	-	-	1.0	pSec	Integrated 12KHz~20MHz	
Duty Cycle		TH/T	45	~	55	%		
Start-up Time		Tosc	-	-	2	mSec		
Aging		ppm/yr	±3			ppm/yr	1st. Year at 25°C	

Note : The table shows common spec. if you have special spec. requirement , please feel free to contact our salesperson.

DIMENSION(mm)



Solder Pad Layout(mm)

To ensure optimal oscillator performance, place a by-pass capacitor of 0.1μF as close to the part as possible between Vdd and GND pads.

FREQ. STABILITY vs. TEMP. RANGE

Temp.(°C) \ ppm	± 20	± 25	± 50
@ -20~70°C	○	○	○
@ -40~85°C	△	△	○
@ -40~105°C	×	△	○
@ -40~125°C	×	×	△

○: Available △: Conditional ×: Not available
 Inclusive of calibration @25°C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration.